

Figure 1

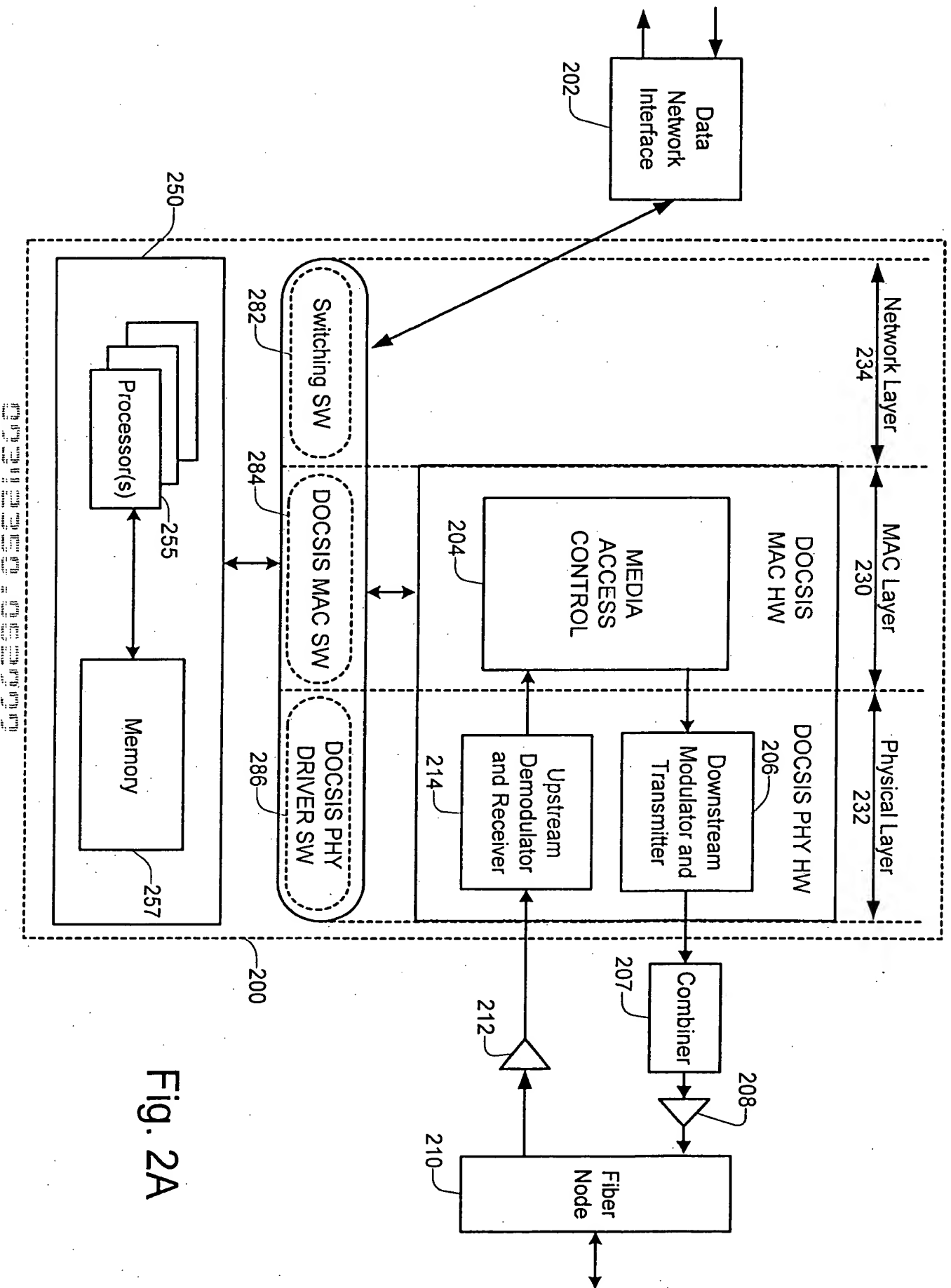


Fig. 2A

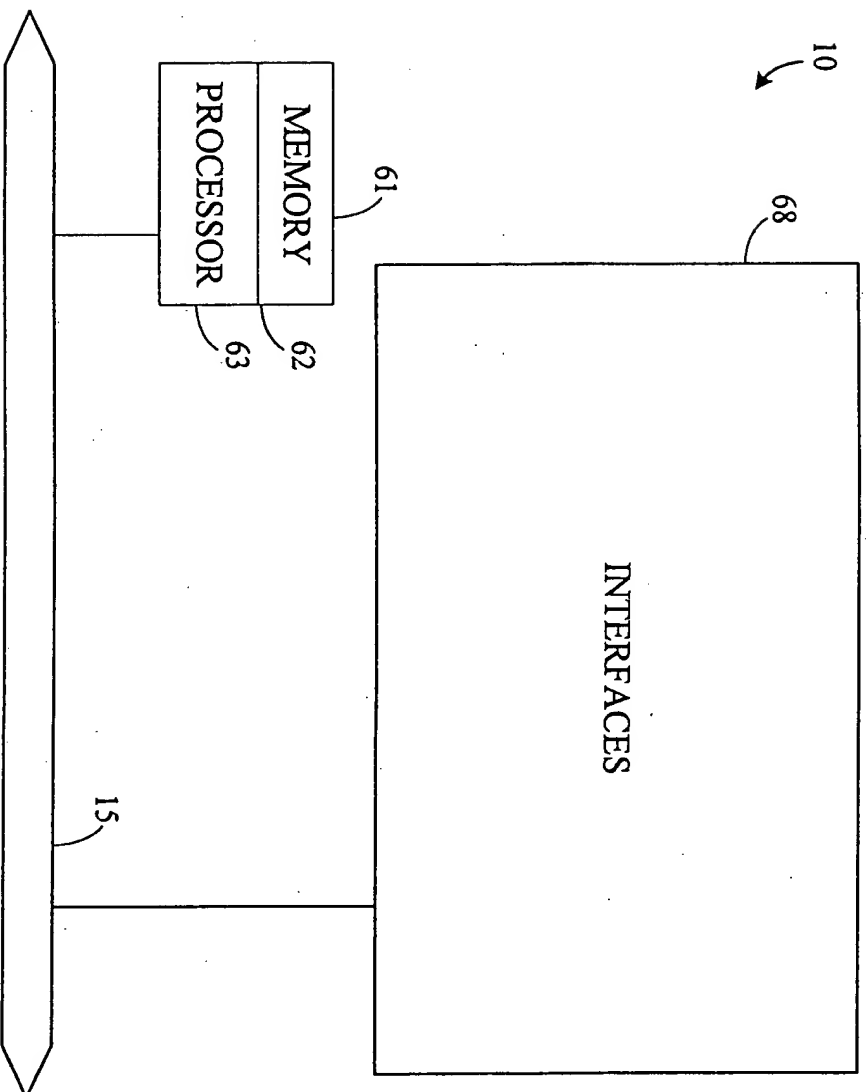


Figure 2B

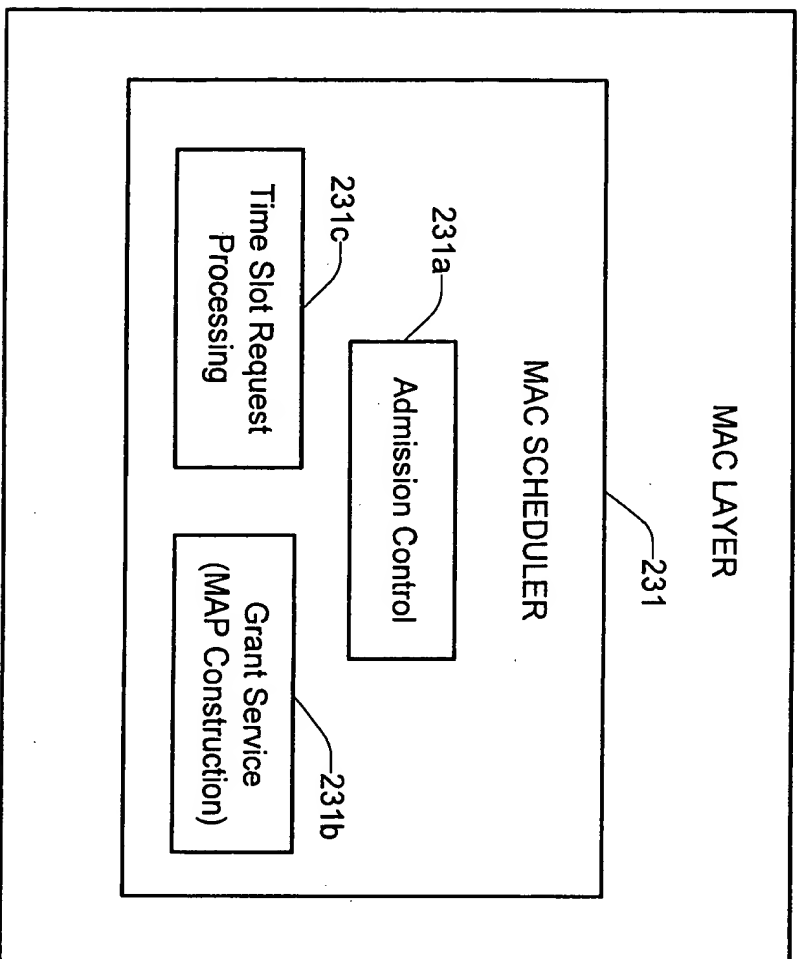


Fig. 2C

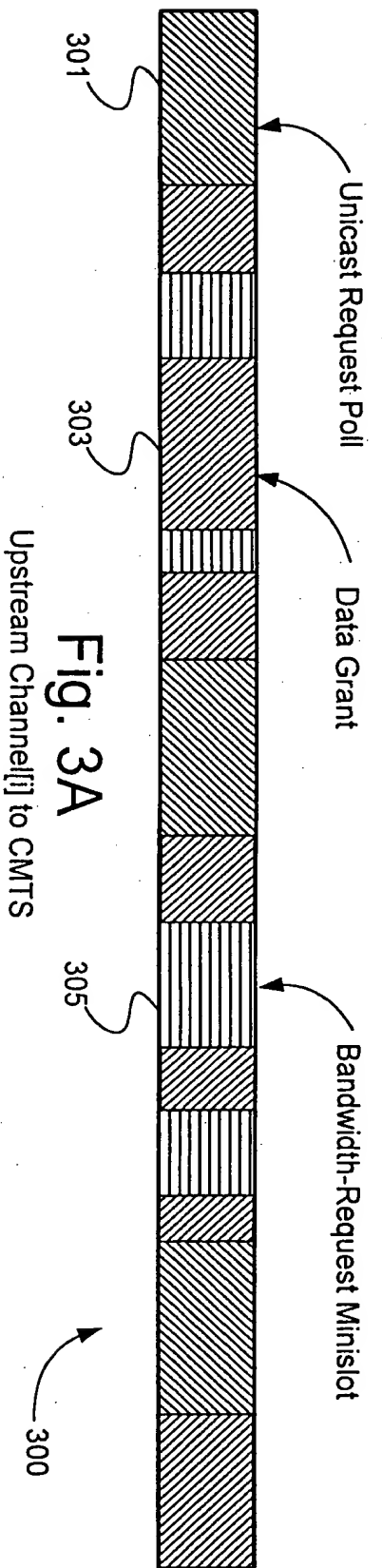


Fig. 3A

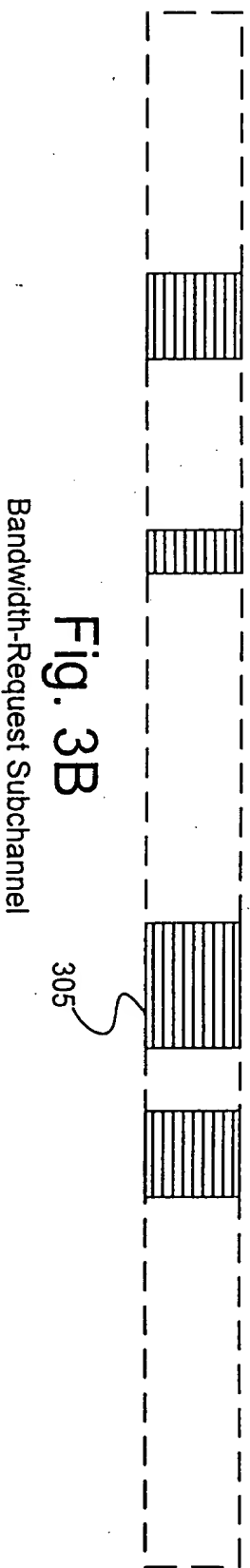


Fig. 3B

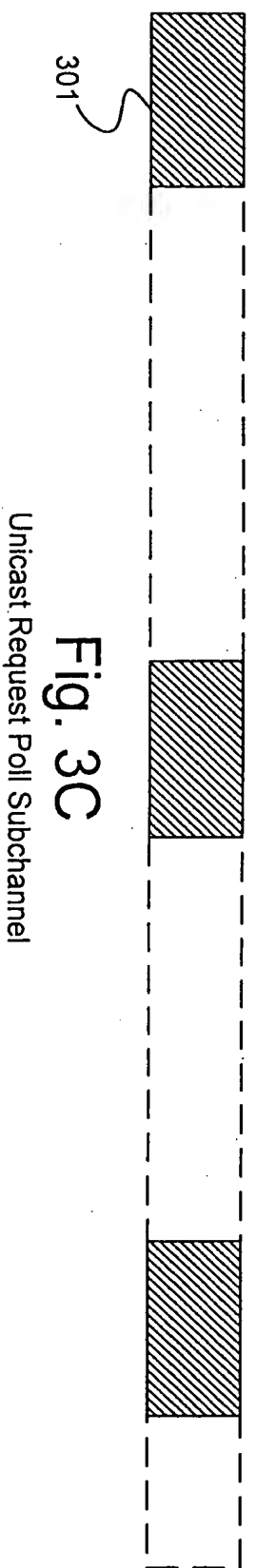


Fig. 3C

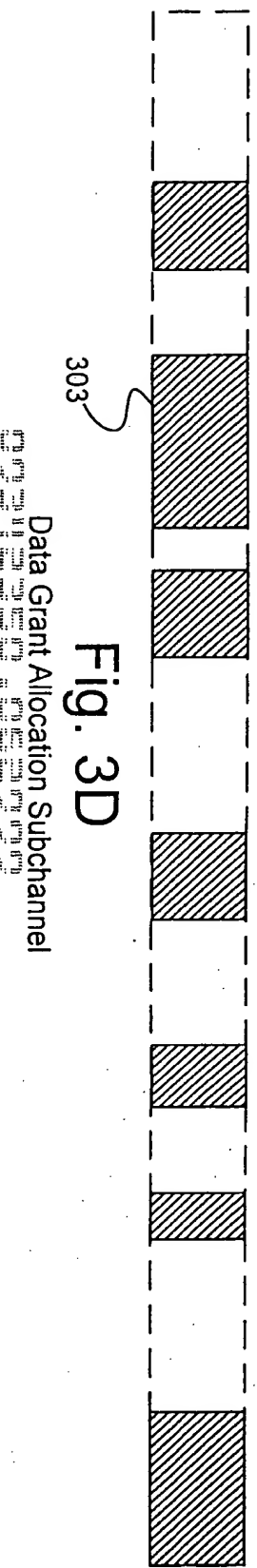


Fig. 3D

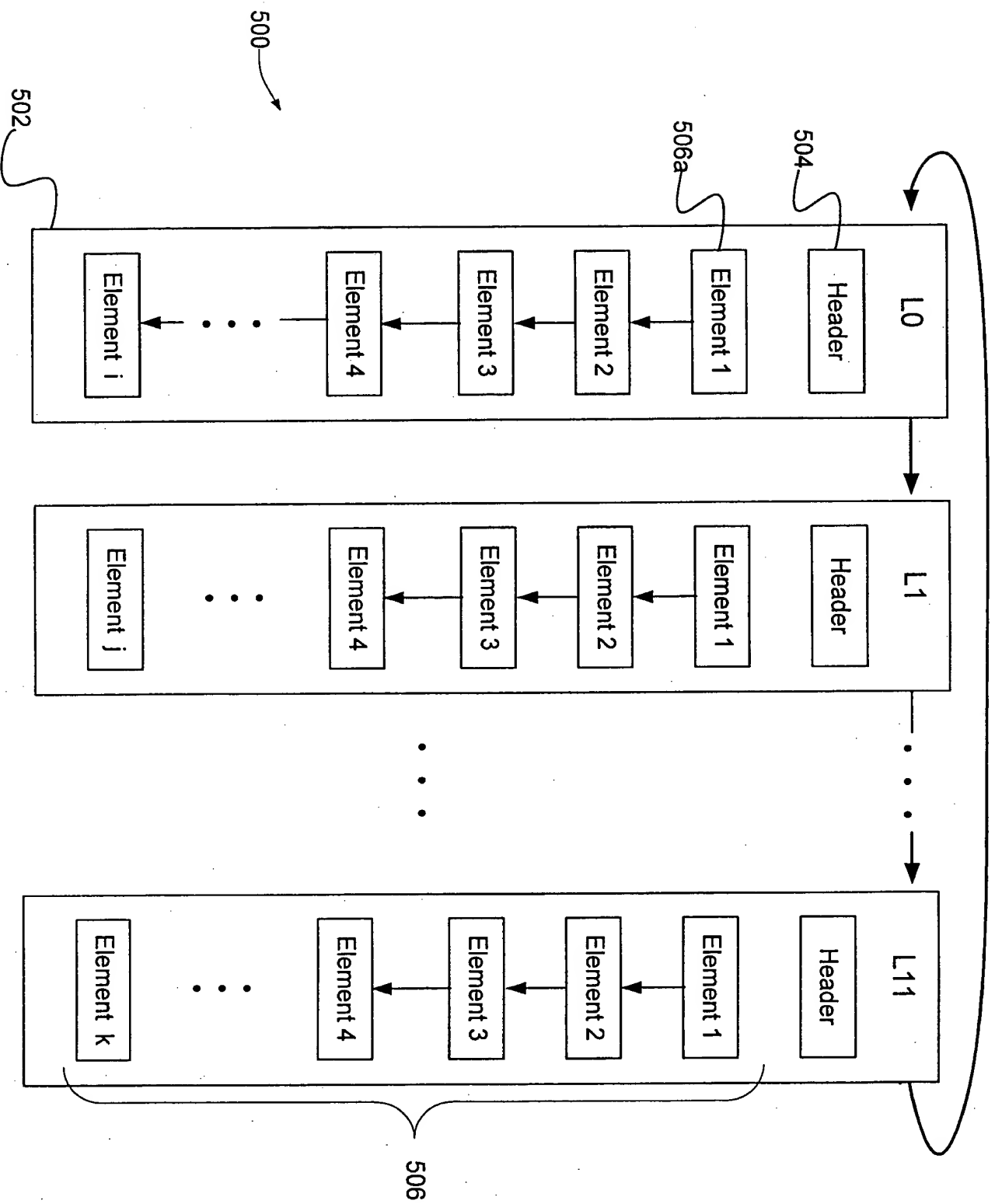


Fig. 5

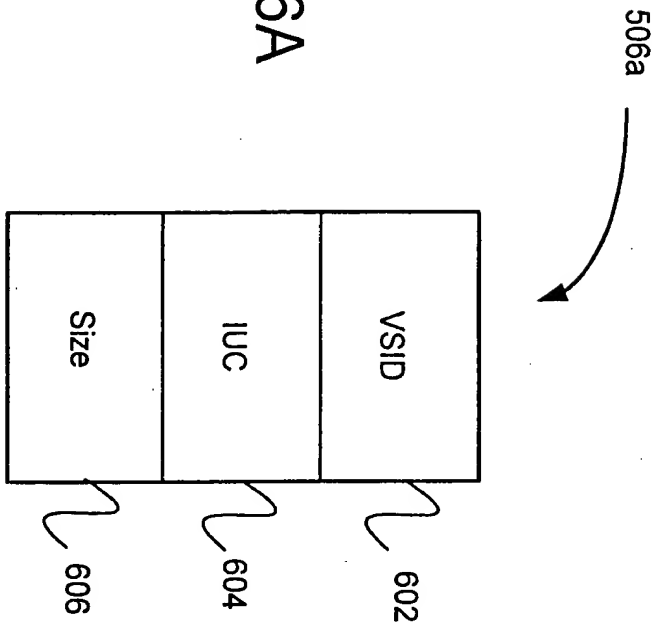


Fig. 6A

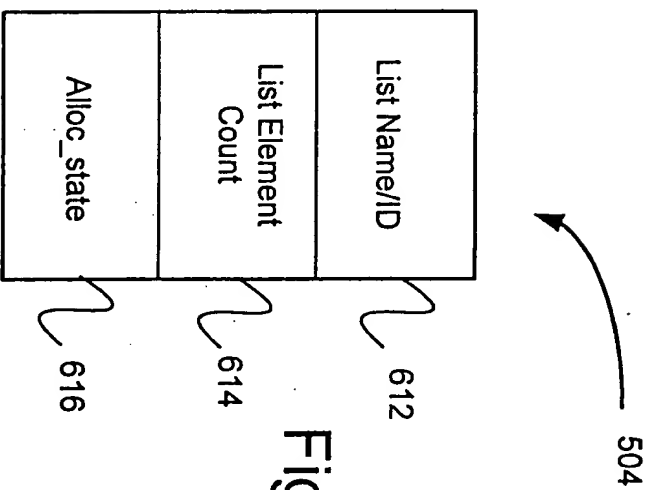


Fig. 6B

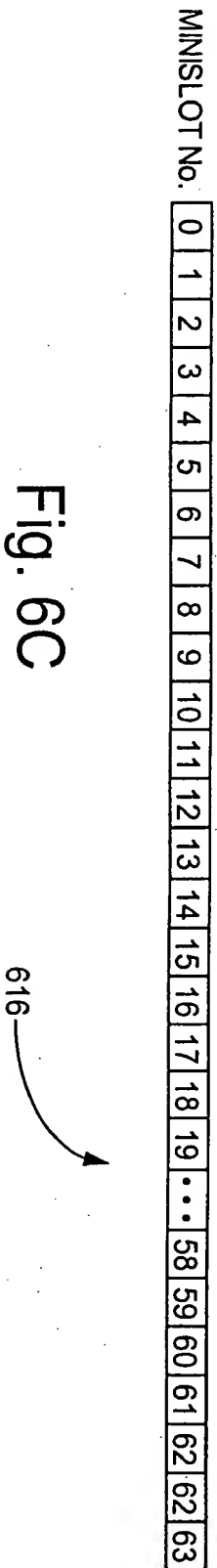


Fig. 6C

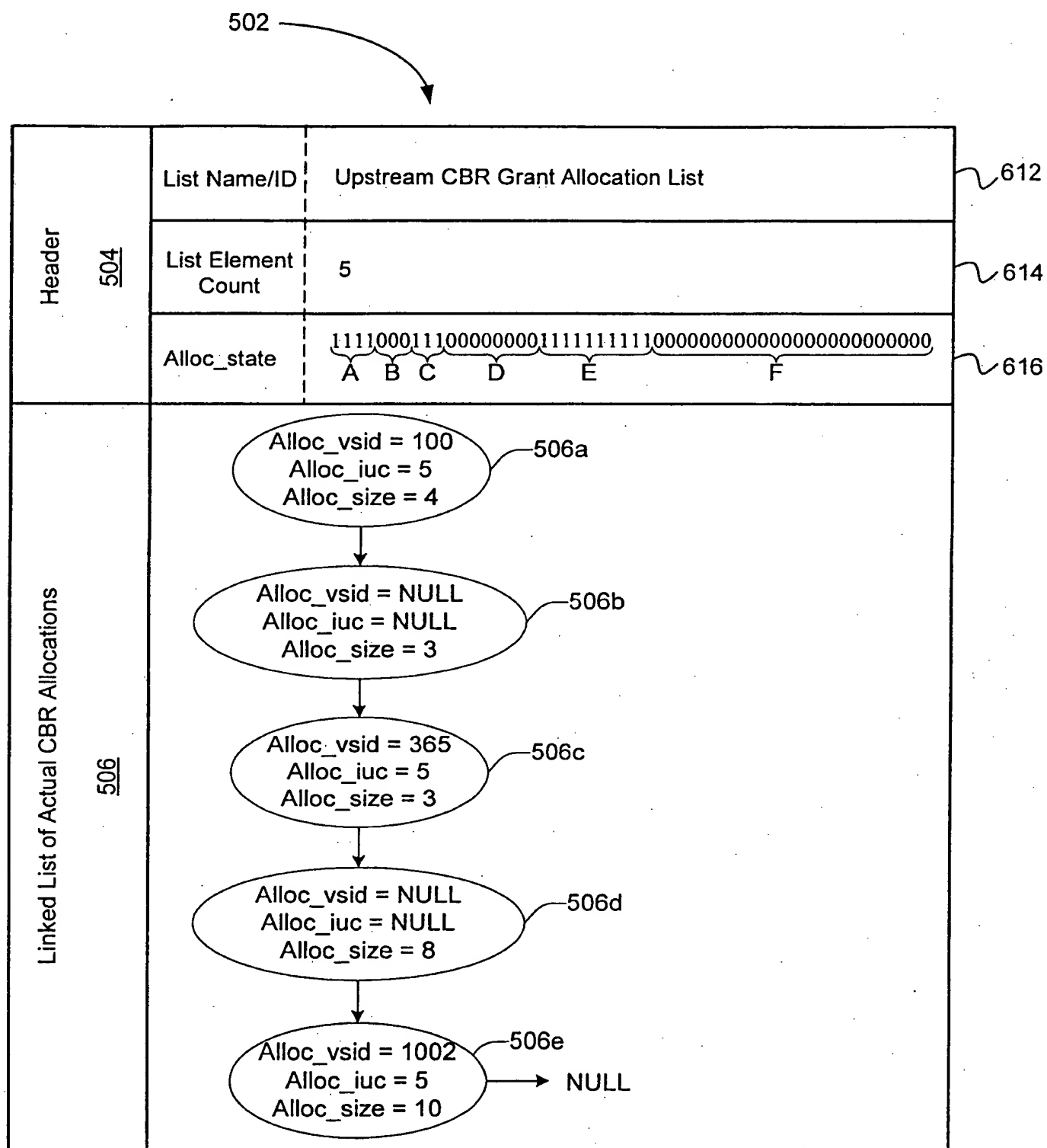


Fig. 7

MINISLOT No.	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	...	58	59	60	61	62	63	
Alloc_state[0]	1	1	1	1	0	1	0	0	0	1	1	1	1	0	0	0	0	1	...	1	0	0	0	0	0	0	
Alloc_state[2]	1	1	0	0	0	1	0	0	1	0	1	1	1	1	0	0	0	0	1	...	1	0	0	0	0	0	
Alloc_state[4]	1	1	1	0	0	1	0	0	0	1	1	1	1	1	0	0	0	0	1	...	1	0	0	0	0	0	
	⋮																										
Alloc_state[10]	1	1	0	0	0	1	0	0	1	1	1	1	1	1	0	0	0	0	1	...	1	0	0	0	0	0	
Alloc_vacancy	1	1	1	1	0	1	0	0	1	1	1	1	1	1	0	0	0	0	1	...	1	0	0	0	0	0	
Mask A(12)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	...	0	0	0	0	0	0	0	
Mask A(13)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	...	0	0	0	0	0	0	0	
Mask A(14)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	...	0	0	0	0	0	0	0	
Mask A(12) AND Alloc_vacancy	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	...	0	0	0	0	0	0	0	
Mask A(13) AND Alloc_vacancy	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	...	0	0	0	0	0	0	0	
Mask A(14) AND Alloc_vacancy	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	...	0	0	0	0	0	0	0	
MASK B	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	...	0	0	0	0	0	0	0	

Fig. 8

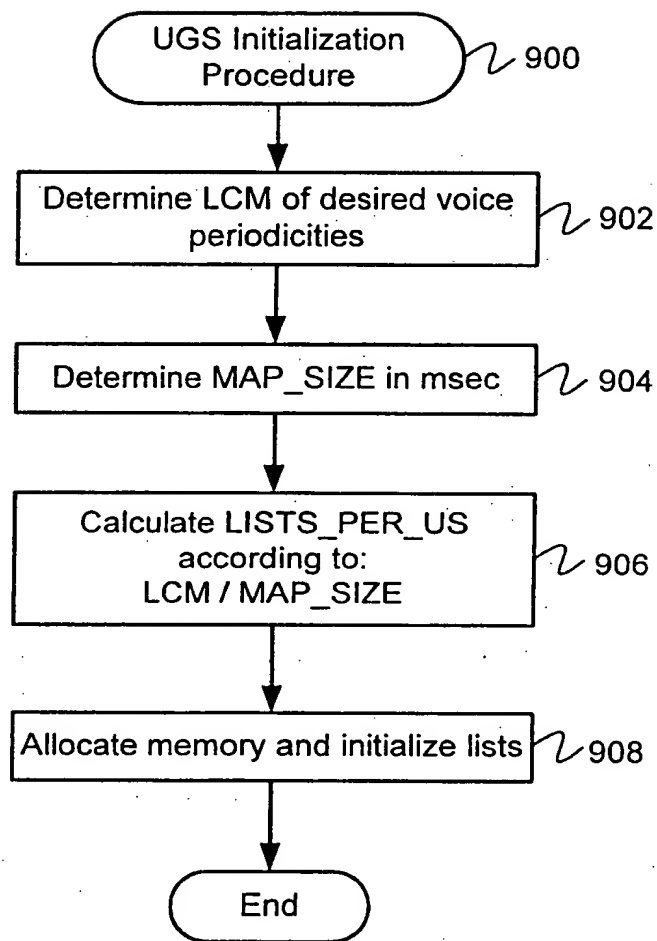


Fig. 9

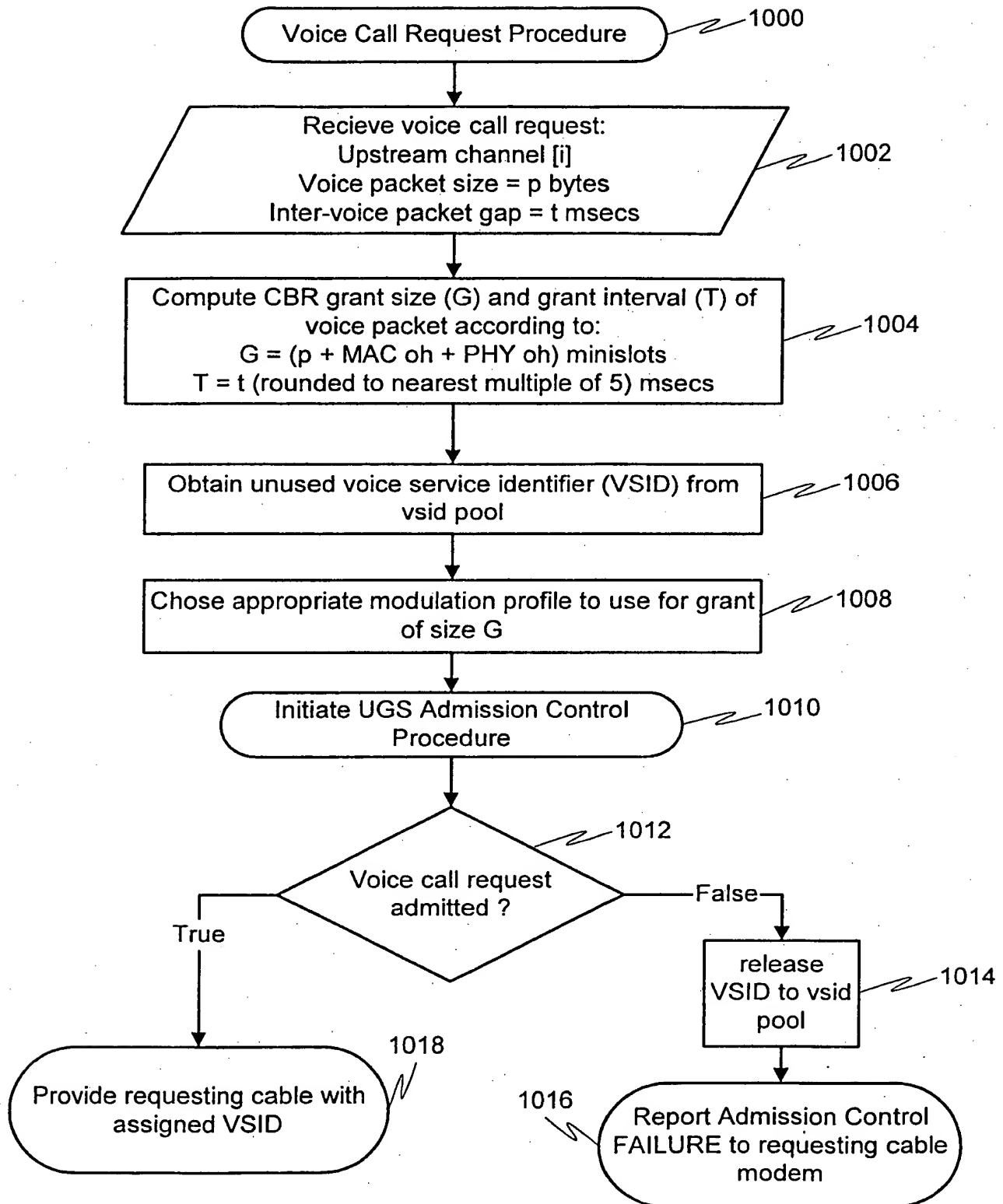


Fig. 10

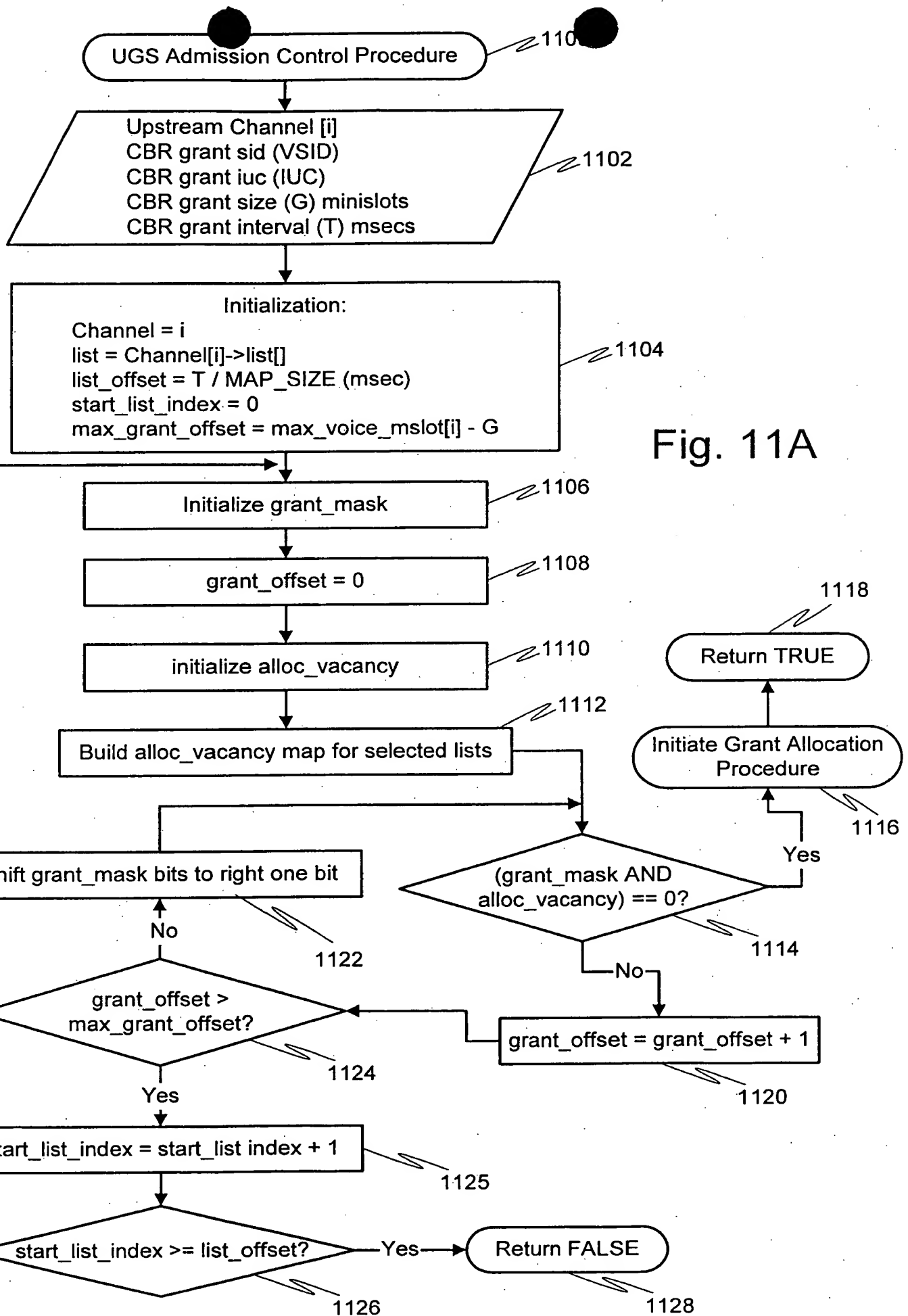
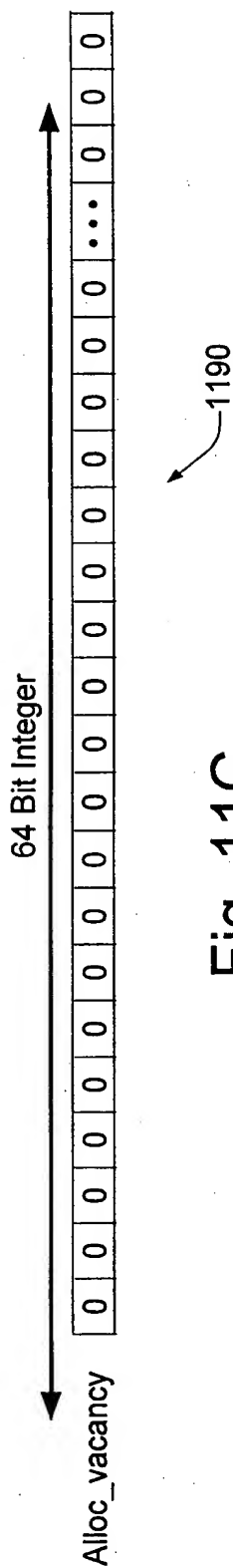


Fig. 11A

-1180



-1190

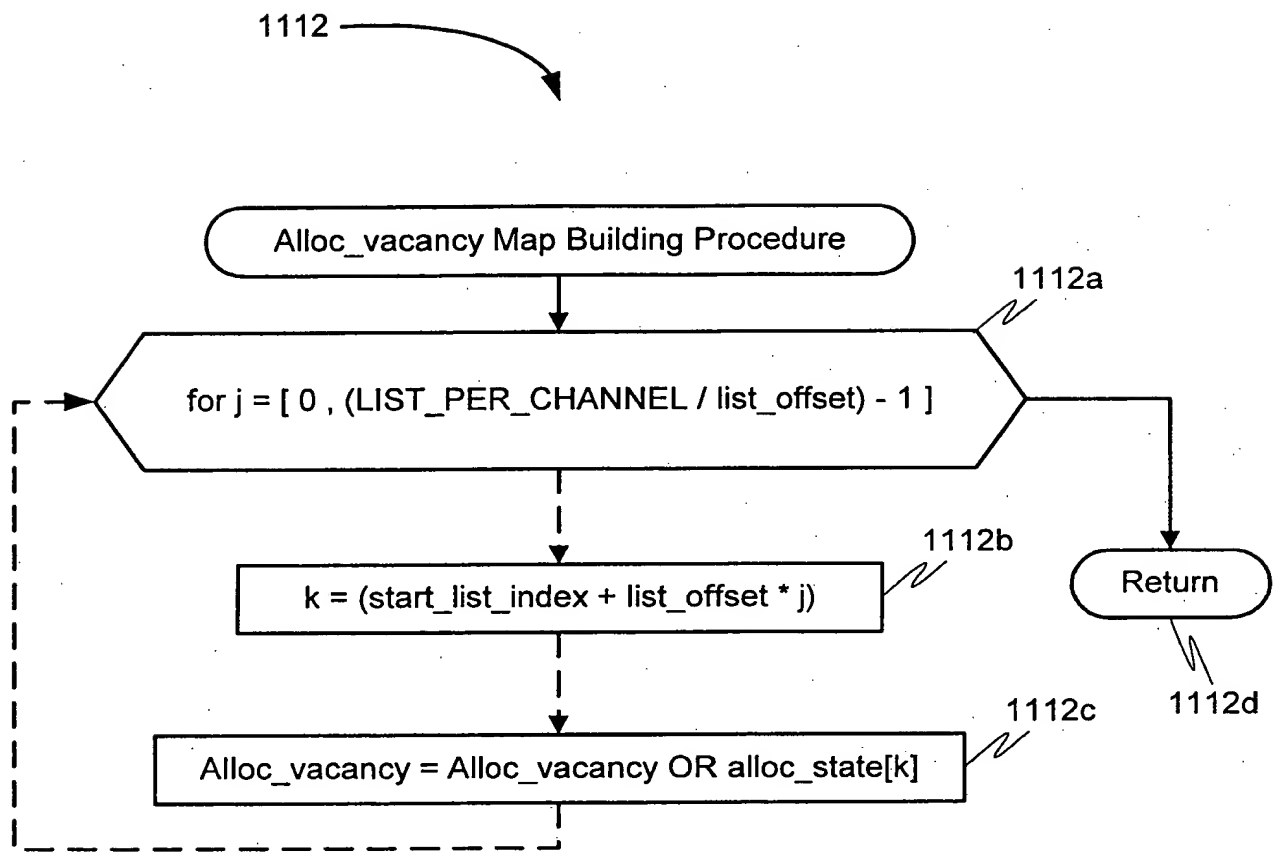
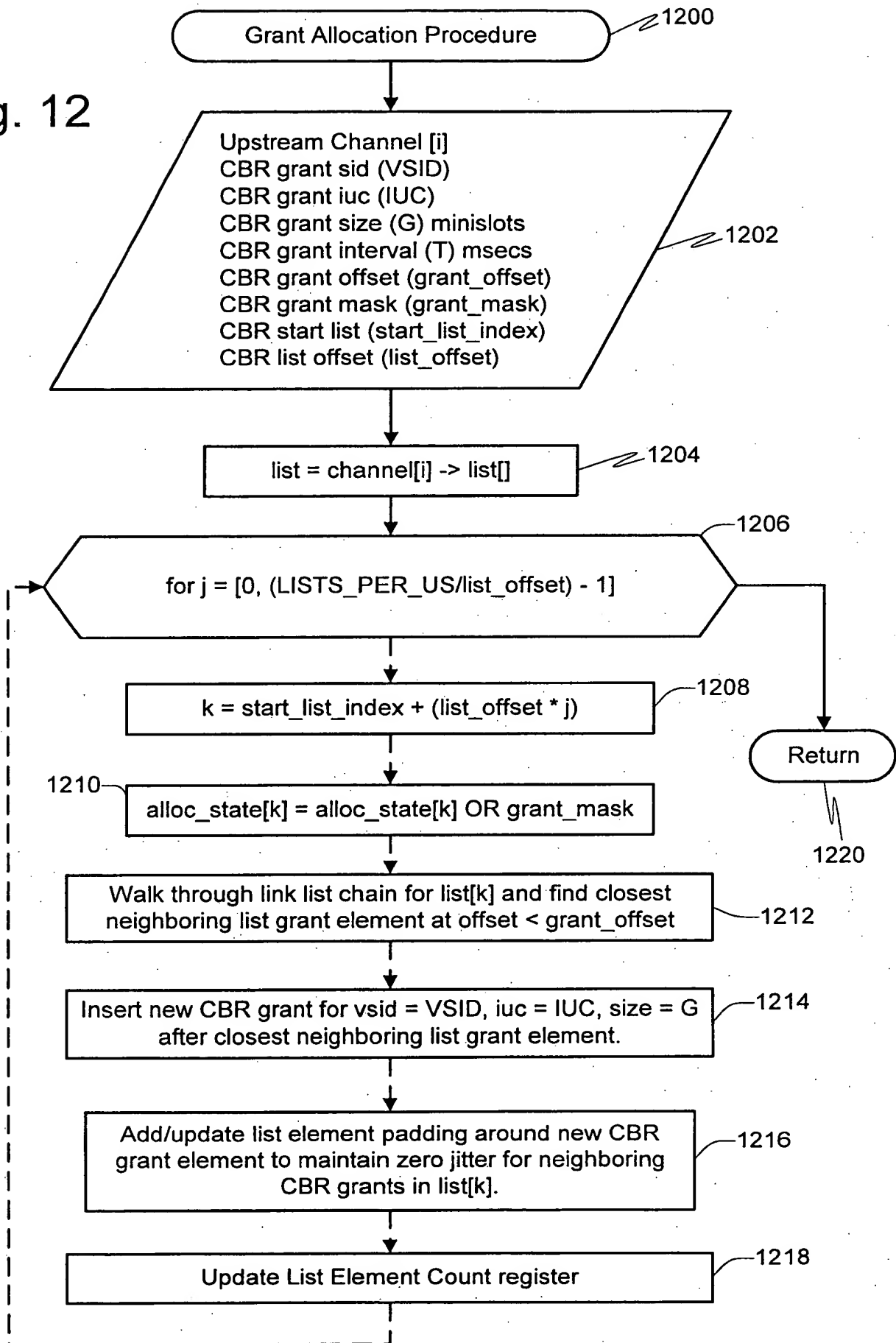


Fig. 11D

Fig. 12



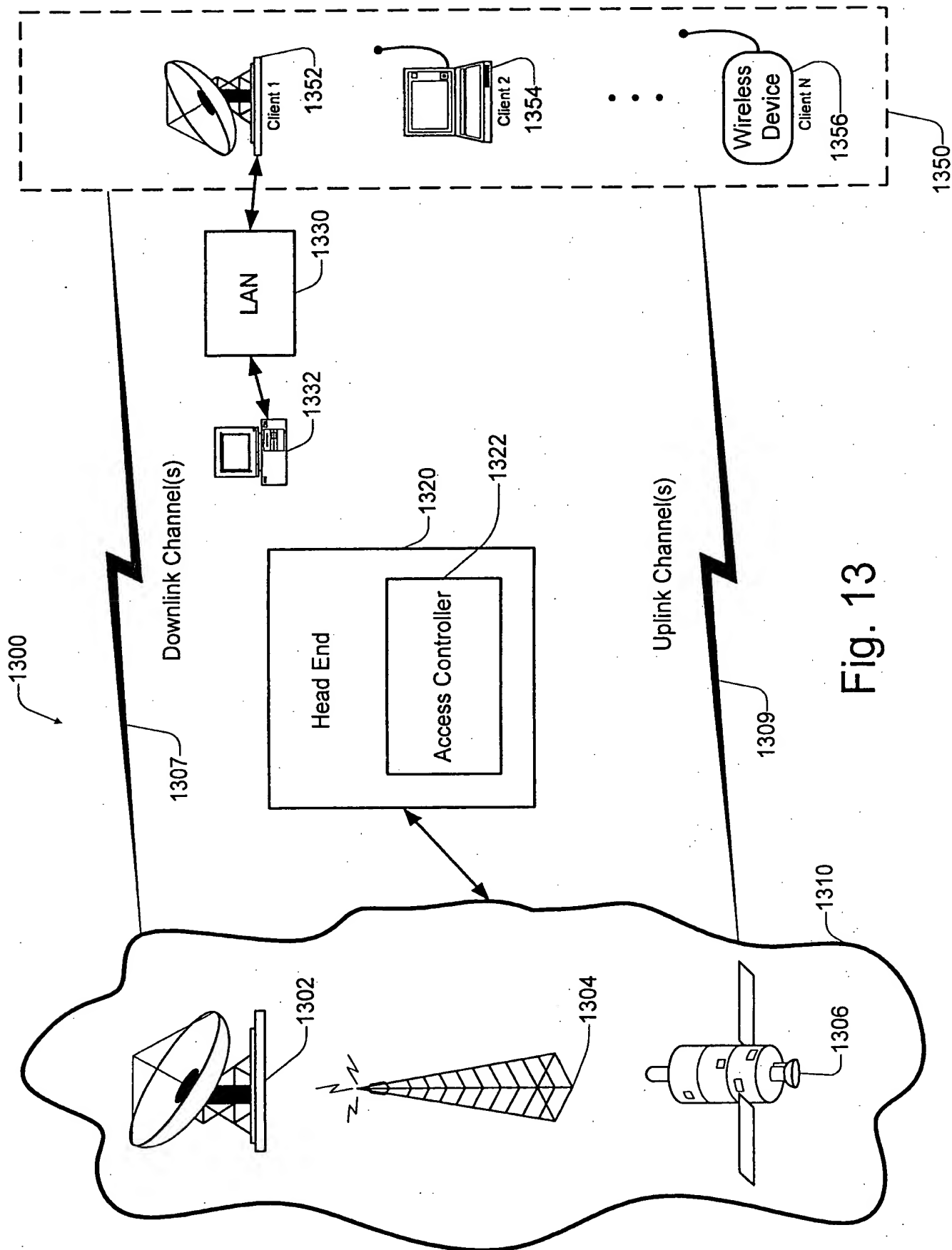


Fig. 13